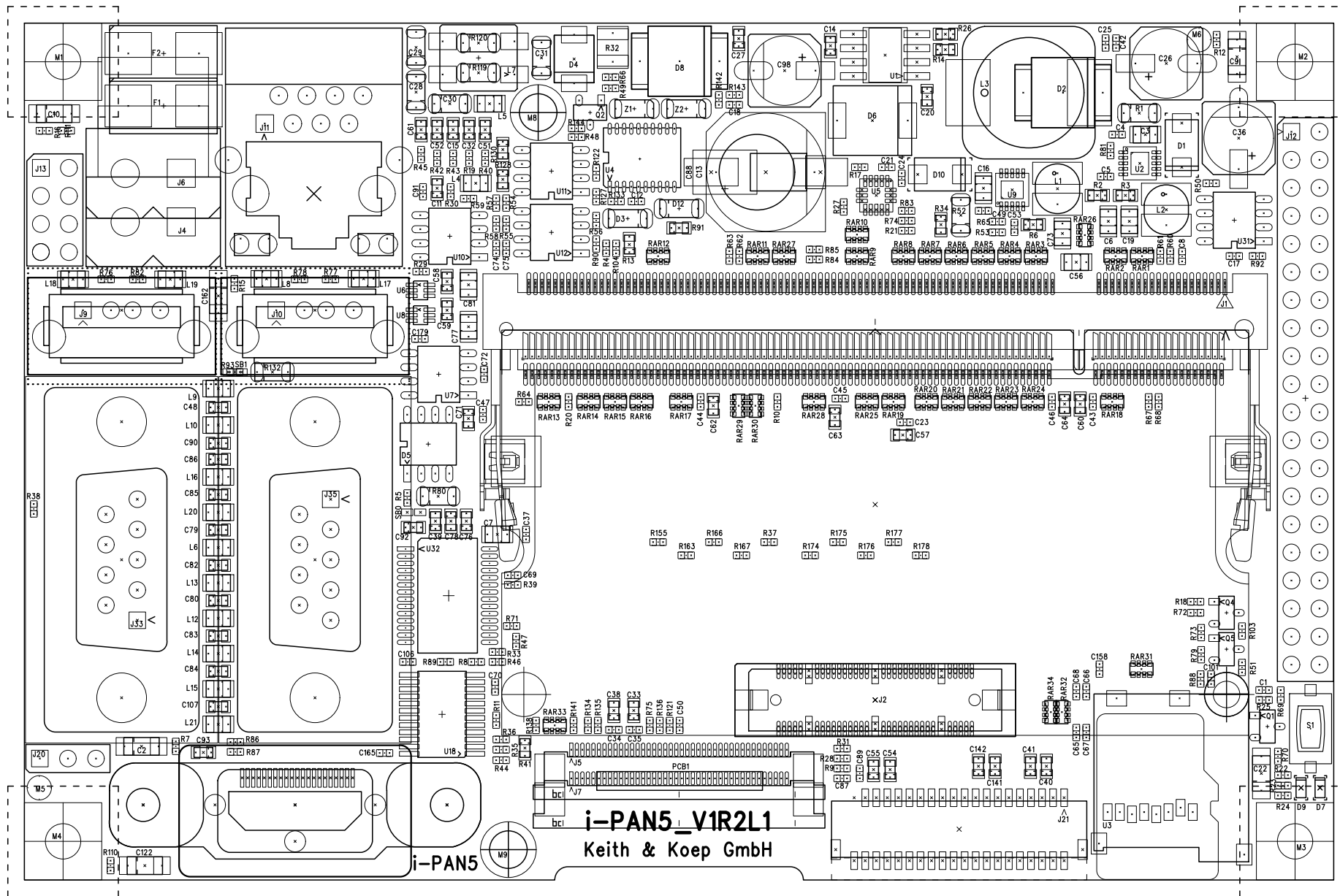


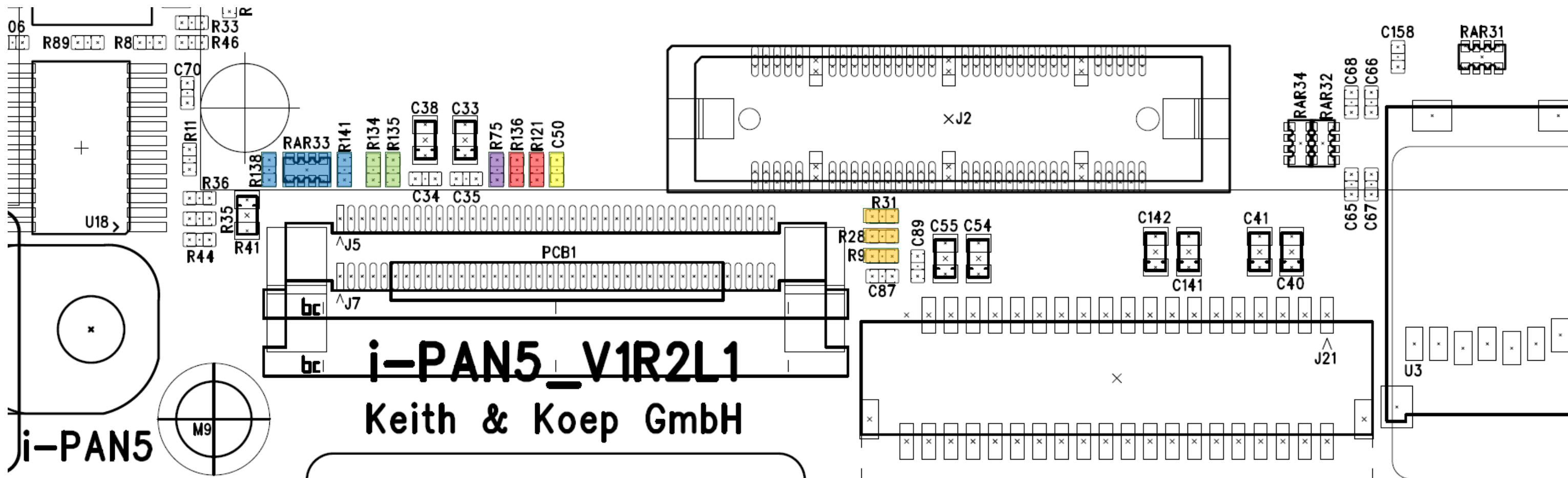
i-PAN5_V1R2L2



Keith & Koep GmbH L1
i-PAN5_V1R2L1 17.04.2013
128,5mm x 77,6mm (brutto)
118,5mm x 77,6mm (netto)

	kapazitiv	kapazitiv	kapazitiv	kapazitiv	resistiv	resistiv	resistiv	resistiv	resistiv	ohne	ohne	ohne	ohne	ohne
	7,0	5,7	4,3	3,5	7,0	5,7	5,0	4,3	3,5	7,0	5,7	5,0	4,3	3,5
Pin	ETM0700G0DH6	ETMQ570G2DH6	ETM0430G0DH6	ETM0350G0DH6	ET0700G0DH6	ETV570G2DHU	ET0500G0DH6	ET0430G0DH6	ET0350G0DH6	ET0700G0DM6	ETV570G2DMU	ET0500G0DM6	ET0430G0DM6	ET0350G0DM6
1	NC	\RESET	NC	\RESET	NC	\RESET	\RESET	NC	\RESET	NC	\RESET	\RESET	NC	\RESET
2	\RST	\RST	\RST	\RST	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
3	B5	B5	B5	B5	B5	B5	B5	B5	B5	B5	B5	B5	B5	B5
4	B4	B4	B4	B4	B4	B4	B4	B4	B4	B4	B4	B4	B4	B4
5	B3	B3	B3	B3	B3	B3	B3	B3	B3	B3	B3	B3	B3	B3
6	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2	B2
7	B1	B1	B1	B1	B1	B1	B1	B1	B1	B1	B1	B1	B1	B1
8	B0	B0	B0	B0	B0	B0	B0	B0	B0	B0	B0	B0	B0	B0
9	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
10	G5	G5	G5	G5	G5	G5	G5	G5	G5	G5	G5	G5	G5	G5
11	G4	G4	G4	G4	G4	G4	G4	G4	G4	G4	G4	G4	G4	G4
12	G3	G3	G3	G3	G3	G3	G3	G3	G3	G3	G3	G3	G3	G3
13	G2	G2	G2	G2	G2	G2	G2	G2	G2	G2	G2	G2	G2	G2
14	G1	G1	G1	G1	G1	G1	G1	G1	G1	G1	G1	G1	G1	G1
15	G0	G0	G0	G0	G0	G0	G0	G0	G0	G0	G0	G0	G0	G0
16	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
17	R5	R5	R5	R5	R5	R5	R5	R5	R5	R5	R5	R5	R5	R5
18	R4	R4	R4	R4	R4	R4	R4	R4	R4	R4	R4	R4	R4	R4
19	R3	R3	R3	R3	R3	R3	R3	R3	R3	R3	R3	R3	R3	R3
20	R2	R2	R2	R2	R2	R2	R2	R2	R2	R2	R2	R2	R2	R2
21	R1	R1	R1	R1	R1	R1	R1	R1	R1	R1	R1	R1	R1	R1
22	R0	R0	R0	R0	R0	R0	R0	R0	R0	R0	R0	R0	R0	R0
23	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
24	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK	PCLK
25	\WAKE	\WAKE	\WAKE	\WAKE	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
26	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC	HSYNC
27	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC	VSYNC
28	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN	Data_EN
29	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H	PWRCTRL H
30	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3
31	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
32	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND
33	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3
34	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3	3V3
35	\INT	\INT	\INT	\INT	NC	NC	NC	NC	NC	NC	NC	NC	NC	NC
36	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL	LEDCTRL
37	I2C_SCL	I2C_SCL	I2C_SCL	I2C_SCL	YU	YU	YU	YU	YU	NC	NC	NC	NC	NC
38	NC	NC	NC	NC	XL	XL	XL	XL	XL	NC	NC	NC	NC	NC
39	I2C_SDA	I2C_SDA	I2C_SDA	I2C_SDA	YD	YD	YD	YD	YD	NC	NC	NC	NC	NC
40	NC	NC	NC	NC	XR	XR	XR	XR	XR	NC	NC	NC	NC	NC

Display size	5.0"	5.7"	5.7"	7.0"	7.0"
Touch variant	res	res	cap	res	cap
populate	R9 R31 R136 RA33	R9 R31 R136 RA33	R9 R28 R121 R135 R134 R138 R141	R31 R136 RA33	R28 R121 R134 R135 R138 R141
DNP	R28 R75 R121 R134 R135 R138 R141	R28 R75 R121 R134 R135 R138 R141	R28 R75 R136 RA33	R9 R28 R75 R121 R134 R135 R138 R141	R9 R31 RA33 R75 R136



EDT_Family_Concept (resistive and capacitive)

3.5", 4.3", 5.0" ,5.7" ,7.0"

