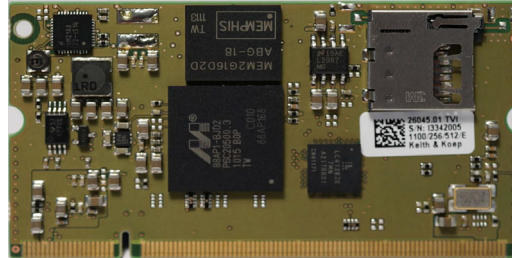


Change Notice:

Trizeps-VI v1r1 to: Trizeps VI v1r2



1.0 Introduction

Keith & Koep improved the Trizeps-VI design for better manufacturability. For taking also technical profit out of this release, Keith & Koep decided to improve minor details.

2.0 Differences between

Differences between Trizeps-VI v1r1 and Trizeps-VI v1r2

1. Internal SD-Card CLK now routed to LCD_DD23(MFP83) instead of MMC4_CLK (EXT_WAKEUP)
2. added pulldown PXA168.N03 Extwakeup
3. removed pulldown PXA168 JTAGSEL
4. pullup/down on DDR DQS signals for better hibernate support
5. improved core voltage layout, noise and Vosc coupling to run at lower voltage

3.0 Software Dependancies

1. For item 2.1 an actualized Bootloader is necessary to output SDCLK on MFP83 instead to EXT_WAKEUP. OS-Images are unaffected. If you are using complete Flashdumps including old bootloader revisions for deployment, please update the bootloader after the deployment of the flashdump and dump the imagefile again for future deployment.

4.0 Affected Ordercodes

TABLE 1.

Old Ordercode	New Ordercode	Module Skew
26025	26025.01	Trizeps VI/168/C1100/R128/N512/ETH/COD/ROHS
26045	26045.01	Trizeps VI/168/C1100/R256/N512/ETH/COD/ROHS
26045	26045.01	Trizeps VI/168/C1100/R128/N512/ETH/WB/COD/ROHS
26085	26085.01	Trizeps VI/168/C1100/R256/N512/ETH/WB/COD/ROHS
26125	26125.01	Trizeps VI/166/C800/R128/N512/ETH/COD/ROHS
26145	26145.01	Trizeps VI/166/C800/R256/N512/ETH/COD/ROHS
26165	26165.01	Trizeps VI/166/C800/R128/N512/ETH/WB/COD/ROHS
26185	26185.01	Trizeps VI/166/C800/R256/N512/ETH/WB/COD/ROHS